

ACORN CAMBRIDGE WORKSTATION



The Acorn Cambridge Workstation has been designed specifically for scientists, engineers and users requiring more than just another micro.

This is the new generation of desktop computing, enabling the individual user to run large, complex programs in industry standard, high level languages - previously only possible with mainframes and super-minicomputers - but without the delays of timesharing.

Fast, 32 bit processing gives the computational performance of a super-minicomputer with the freedom and response of a micro.

The Workstation user has a complete system free from reliance on centralised computing services, with massive improvements in personal productivity. Communications are available to other Workstations or to super-minicomputers to remove the limitations of standalone micro systems.

The Acorn Cambridge Workstation will enhance and improve existing central computing facilities - not make them redundant.

THE CAMBRIDGE WORKSTATION

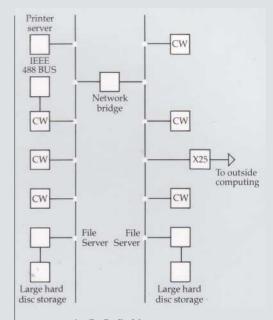
The current generation of microcomputers are used, mostly, for tasks which are not computationally demanding. Applications such as wordprocessing and simple spreadsheet analysis use the organisational features of micros rather than any true numerical capabilities. The emergence of 16-bit personal computers gave the *expectation* of much improved computing performance, allowing the micro to tackle applications previously confined to supermini and mainframe computers.

However, the *reality* has been that the newer processors offer more memory accessibility (but typically less than 1Mb), allowing larger programs to be run, but no real advances in true computing power; in fact one of the commonest implementations is actually slower than some prominent 8 bit micros.

One of the main factors determining the performance levels of personal computers is their heritage of both hardware and software. Chip designers were constrained by a desire to maintain a degree of software compatibility with 8 bit product ranges and software designers used that compatibility to update 8 bit software rather than start afresh.

The Acorn Cambridge Workstation transcends these limitations. The 32 bit processor is a completely new design owing more to supermini design than to previous microcomputer designs. The operating system software is also new but, from the end users point of view, there is one important link to the past.

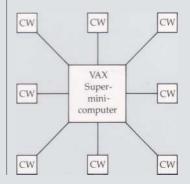
Not, however, a link to microcomputers but to supermini and mainframe computers: that link is the languages that are standard with the Workstation at no extra charge. The languages are the four most popular with scientific computer users and the one ever popular in the increasing field of artificial intelligence research.



A C O R N DISTRIBUTED COMPUTING

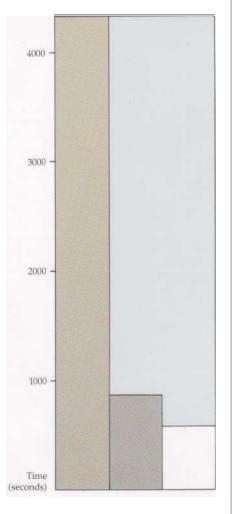
The Acorn Cambridge Workstation can form the basis of a distributed computing network, using Acorn fileservers or existing facilities as a file store. Each Workstation has a network interface as standard and a X-25 gateway is available for access to central computing or other X-25 packet-switched networks - long distance and even international intercomrunuication is possible via public packet-switched networks

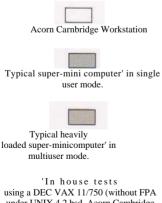
DIRECT SUPER-MINI COMPUTER (VAX) LINK To take the strain off an overloaded super-minicomputer, a direct link to a DEC VAX is available. Files may be transferred to a VAX via serial lines, or the VAX filing system can be accessed transparently by the Workstation user as if it were a local file store. Files stored on a VAX can be archived in the usual manner.

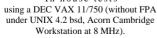


SPICE -A CIRCUIT SIMULATION PROGRAM

(17000 lines FORTRAN)







The Acorn Cambridge Workstation is designed as a general purpose computer to provide high level computing power on the desk top, not at the desk top. The user is in complete control of the environment in which programs are run, with no interference and delay necessary to service the demands of other system users. Thanks to the fast and powerful National Semiconductor Series 32000 chip set, 32 bit processing using industry standard languages can be provided on the desk top. The languages are, FORTRAN 77, ISO PASCAL, C and Cambridge LISP. These are full implementations of the languages not subsets tailored for use on micros. The Workstation hardware provides the exact environment these languages were devised to run in, a large contiguous address space, hardware floating point processing, and a fast error correcting hard disc unit. Of course, being an Acorn product we also include BBC Basic.

The porting of existing mainframe and supermini applications software can be a painless procedure, since the same high level languages are available.

The surprisingly compact, two-box design combines a high resolution, high contrast, direct etched monitor, processors, 1 Mbyte of memory, network interface, 640 Kbyte (double density) floppy disc and/or 20 Mbyte Winchester disc all in one unit with a separate, fully equipped 92 key keyboard. All interface connectors are on the back panel and memory expansion up to 4 Megabytes is internal.

Terminal emulation for VT52, VT100 and Tektronix 4010 is also available in ROM.

PERFORMANCE

The 32 bit processor was designed to be a mainframe in silicon. The internal architecture closely models super-minicomputer construction and is optimised with software in mind, giving excellent support for high level language compilers. Included is an IEEE standard floating point support processor (NS32081) providing both single and double precision arithmetic (32 bit/64 bit). Benchmarks generally give only a very narrow picture of computer performance. A better measure is real applications, such as SPICE shown opposite. With a typical, fully loaded, multiuser super-minicomputer it may be possible for a user to get only a single run/day; with the Acorn Cambridge Workstation an order of magnitude improvement is possible - a real increase in personal productivity.



SOFTWARE - LANGUAGES

Extensions to the standard: - hexadecimal constants - character & in identifiers Fortran 66 compatibility (as option) - DO loops execute at least once - Hollerith constants in DATA and CALL - quoted constants not of type CHARACTER Single precision (32 bit) and double precision (64 bit reals; option for 64 bit single precision) Post- mortern backtrace facility showing source line numbers Trace facility Fully compiled
Extensions include machine language inserts Integers are represented in 32 bits (16 bits for shorts), floating point numbers in 32 bits (64 bits for doubles), characters in 8 bits. C library implements the following functions (and macros) as described in UNIX bsd 4.2 documentation: - Section 3 - where appropriate - Section 3S (10) - as complete as possible - Section 3M (maths) - majority - Section 2 - calls to operating system primitives possible Fully compiled
Extensions to the standard include: - characters 'S' and '-' in identifiers - non-decimal constants - bit vector operators - some system programming functions - OTHERWISE clause in CASE statement - 'include' files - machine language inserts - separate compilation facility with MODULE, IMPORT, and ALIAS constructs Real numbers stored in double precision (64 bit) Post-mortern backtrace facility showing source line numbers Checks for unassigned variables possible Fully compiled
Multi-length arithmetic package limited only by the machine's memory Interpreter and compiler Full screen editor Additional constructs based on Common Lisp proposals
Upwards compatible with BASIC 1 and other earlier versions and contains the following enhancements: - LISTIF command - improved LIST formatting - 10 digit precision of printing - extension to EXT# - OPENUP function - OSCLI statement - ON PROC statement Full screen editor; To retain compatibility, operates as a stand alone system Floating point operations carried out in software with the same precision as

Floating point operations carried out in software with the same precision as BBC Microcomputer BASIC Interpreted

FORTRAN 77

Conforms to ANSI X3.9-1978/ISO 1539-1980 (E)

С

The language conforms closely to that as described in the book 'The C Programming Language' by Kernighan and Ritchie, published by Prentice-Hall, 1978

PASCAL

Conforms to BS 6192:1982, 'ISO Pascal Level 1' Category A, (also known as ISO 7185)

LJSP

Cambridge Lisp

BBC BASJC

Fully compatible with BBC Microcomputer BASIC

A C O R N AND THE CAMBRIDGE CONNECTION

Founded at Carnbridge in 1978, Acorn is now one of the most mature companies at the forefront of Britain's technology and communications revolution. Acorn's product range started with small computers for the growing enthusiasts market and steadily evolved with more and more sophisticated features. This development was justified when, in 1981, the specification for the proposed BBC microcomputer was published. This complicated and extensive system was ideally suited to the well advanced

ideas Acorn had for the Proton computer. Acorn's endeavours were rewarded with the successful acquisition of the BBC contract despite fierce competition. In 1984 a further 4 year contract was signed.

With growth from a turnover of under £0.5m in the late seventies to £93m in the mid eighties, a key element in Acorn's success has undoubtedly been the company's sustained and continuing research and development effort. Involving over a third of the company, this has been by no means directed solely at the consumer market.

Acorn has strong links with Cambridge University and in particular the Cornputing Laboratory where much of the specialised equipment was developed by, or in conjunction with, Acorn. Development lines cover areas from speech recognition to satellite communications and advanced networking techniques. Acorn is rapidly becoming recognised as a leader in the new technology of designing on silicon.

QUALITY ASSURANCE

Tight quality control and careful production engineering have led to Acorn products being widely recognised for quality and reliability The Workstation hardware has been designed and manufactured to a high standard of reliability and safety and complies with BS 415. The compilers and operating system have been subjected to: - internal acceptance tests - certification by international standards bodies, (NCC/GSA for Fortran 77, BSI for ISO Pascal) - independent audits, and - extensive field trials All software development has utilised high *level* languages



Product Range

A *Powerful Package:* The Acorn Cambridge Workstation has been designed as a complete package of hardware and software, fully equipped to meet the computational demands of engineers and scientists.

Model No:	ACW 100	ACW 121	ACW 143	ACW 443
Monitor	Mono (14")	Mono (12")	Colour (12")	Colour (12'
Floppy disc		2 x 640Kb	640Kb	640Kb
Winchester			20Mb	20Mb
Memory (RAM)	1Mb	1Mb	1Mb	4Mb
Max Memory (RAM)	1Mb	4Mb	4Mb	4Mb
SOFTWARE		HARDWARE		

Standard features with each Workstation at no extra charge:

Operating system: PANOS Languages: FORTRAN 77* ISO PASCAL* C* CAMBRIDGE LISP* BBC BASIC 32000 ASSEMBLER * These are full mainframe implementations, not cut down versions for micros <i>User Guides:-</i> PANOS Guide to Operations PANOS Guide to Operations PANOS Programmer's Reference Manual BBC Basic Reference Manual BBC Basic Reference Manual ISO PASCAL Reference Manual ISO PASCAL Reference Manual C Reference Manual C AMBRIDGE LISP Reference Manual Acorn 32000 ASSEMBLER Reference	Computational processor CPU: NS 32016 Floating Point Unit: NS 32081 ROM: 32 Kbyte RAM: As above <i>I/o Processor</i> CPU: 6502 RAM: 64Kbyte ROM: 32Kbyte Acorn ADFS in ROM + DFS (Version 2) in ROM Network (Econet TM) ROM RS423 Serial port Centronics parallel printer port RGB (TTL) output D-type analogue socket BNC Video 37 way D-type 1MHz bus Keyboard
Manual Function key card booklet	LAN socket User Guides
	Acorn Cambridge Workstation User Guide Filing System Manual



Performance

(1) RAW	Performance		<u>(2) Real</u> Appli	cations	
8 MHz clo	ock speed		SPICE* (A circ	uit simulation pr	ogram)
DANG		•.1 •.		Time (s	seconds)
RAM has a states	access time of 150 ns	with no wait		Acorn Cambridge	VAX 11/750 (withoutFPA)
Full speed	bus cycle in 4 clock of	cycles:		Workstation	under Unix
	ta (16 bits)				4.2 bsd
— write da	ata (16 bits)				
— fetch i	nstructions into queue		RAM Cells	1010	1612
— acknow	wledge interrupt		Two bit		
	r data to/from slave (H	FPU)	counter	3100	4248
0.1. (6.4.1.		5-stage ring		
8 byte prei	fetch queue		oscillator	1710	2743
Typical in	struction times in mi	croseconds at	Differential		
8 MHz for	32 bit (doubleword)	fixed point	sense		
operations	:		amplifier	160	156
move	register to register	0.38		5980	8759
r	memory to memory	2.12	τ.1.	5980	8759
add	register to register	0.50	Job		
	memory to memory	4.25	throughput:		
	register to register	1037	No of runs/	10	1
r	memory to memory	13.75	day in practice	e 10	1
					(if multi-user)
8 and 16 b	it operations are faste	r			
Typical in	struction times in mi	croseconds	Conversion eff	•	
at 8 MHz f	for floating point instr	ructions:	This was to cor	wert 17000 lin	es Fortran IV
		32 bit 64 bit	to Fortran 77		
add regist	er to register	9.25 9.25			
0	ter to register	6.0 7.75	*SPICE, unmod		
-	•	1.13 14.75			teley. This is an
(source: N	ational Semiconducto	or	Scientific Divis		corn Computers
Corporation)		Scientific Division.			



PANOS

Operating system: PANOS has been designed by Acorn for professional users, with advanced mainframe applications and software development in mind. It is in the modern style, being both modular and extensible, being written in the high level language Modula-2.

This modular approach offers the ability to amend, reconfigure or add to parts of the operating system without a total rewrite. Also, because it has been designed for a desktop workstation, it does not consume a great deal of systems resource; which is frequently a problem when much larger operating systems are transported from mainframes and minicomputers.

A feature of PANOS, rarely found with other operating systems, is the ability to support cross language calls for Fortran, C and Pascal. This means that program suites written in any of these languages are available to applications writers working in a different high level language.

PANC	DS supports:	
 program loading and execution — a procedural model of program 		<i>lure library</i> (callable from high programs) consisting of the lules:
 execution, thus programs can invoke other programs — command line interpretation — command files with parameter substitution — event handling — cross language calls — stream based I/O model with filing system treated as part of file name — support for Acorn filing systems: ADFS, NFS and DFS — time stamping on files 	Handler	argument decoding type conversions memory allocation stream based input/output error handling file management system loader time and date handling event, condition handling global string variable management direct access to BBC micro command interpretation program, procedure execution random numbers pattern matching



PANOS

PANOS	PANOS Utilities:				
Linker which:	Further ut	ilities for system configuration,			
 supports independent compilation of modules 	file management and others: access set file or directory permissions				
 supports cross language calling standard accepts input in Acorn Object Format 	catalogue	display file or directory data			
generates output as relocatable images	configure copy	adjust system startup parameters copy files or streams			
Full screen editor with: — arbitrary length lines	create delete	create files or directories delete files or directories			
 search and replace operations selection, delete, move and copy of 	echo	echo expanded argument to screen			
blocks	help	provide on-line help			
 undelete operation multiple windows and multiple buffers 	move set	move a file set system parameters			
 command window providing access to command interpreter 	show star	display system parameters execute a'*' command			

Installation command files to facilitate installation from the distribution media





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