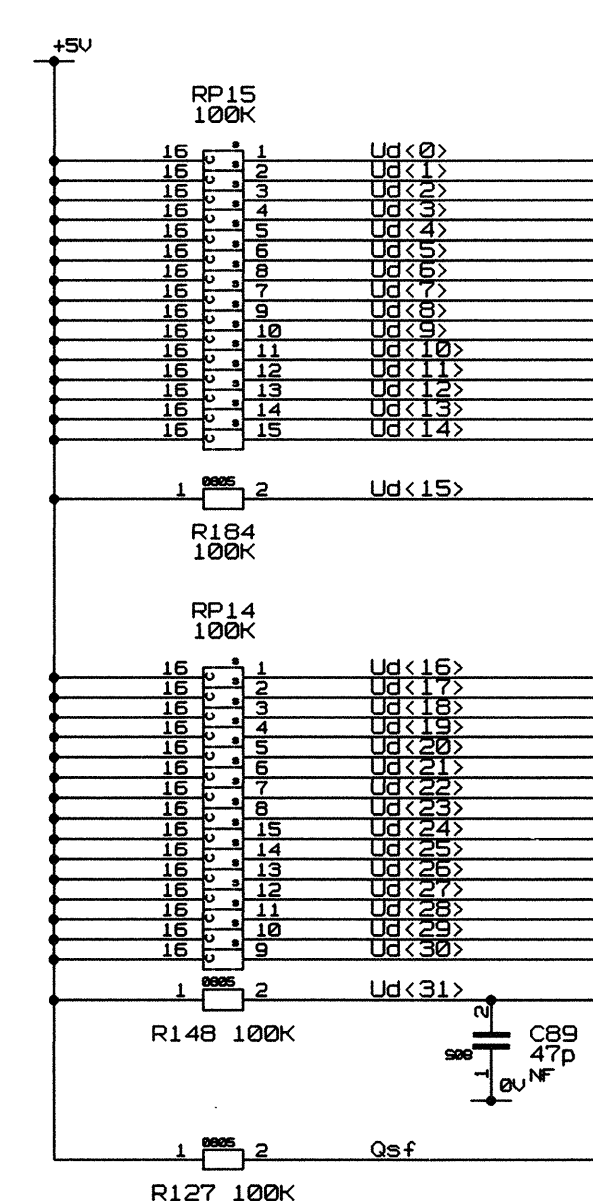
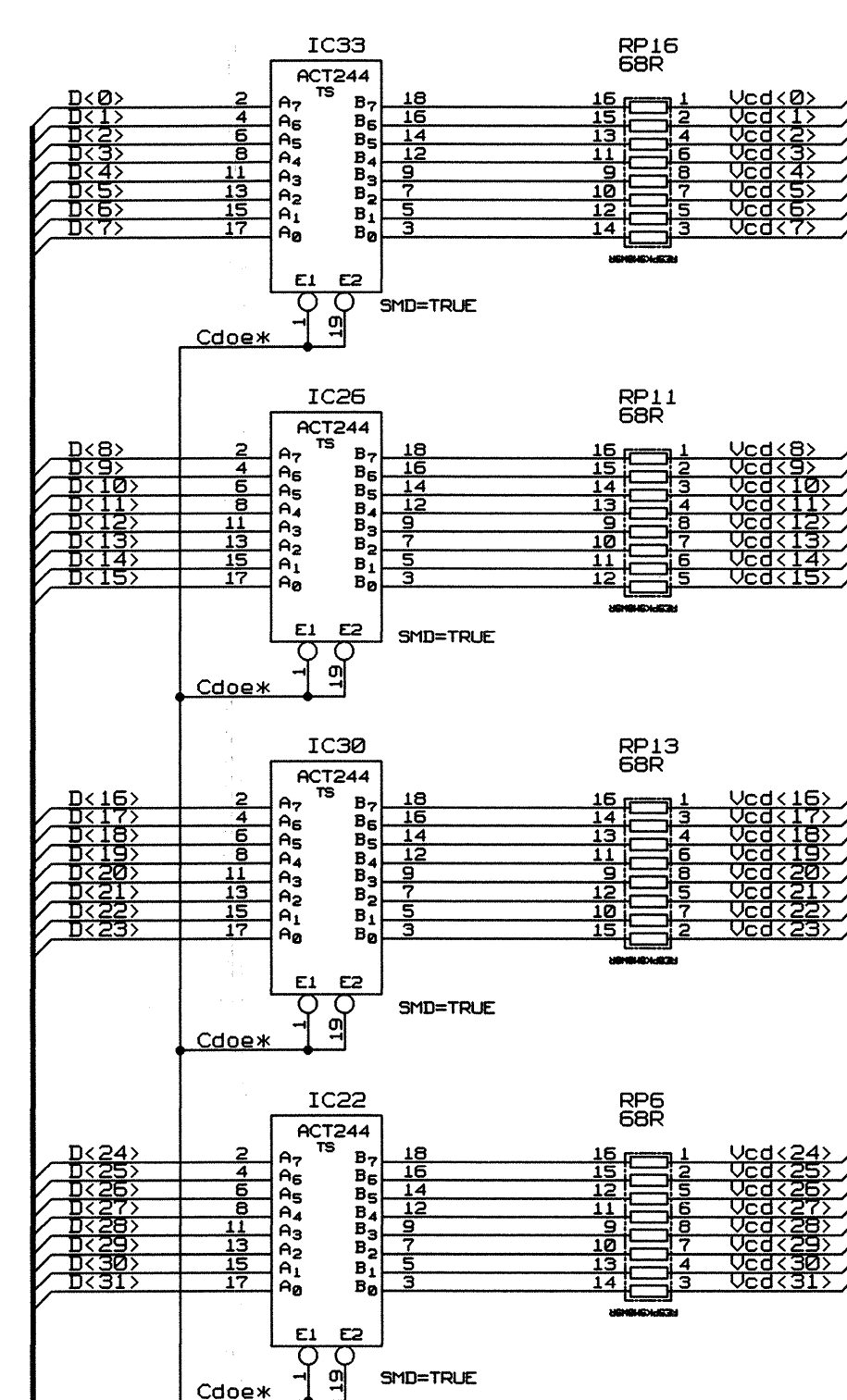


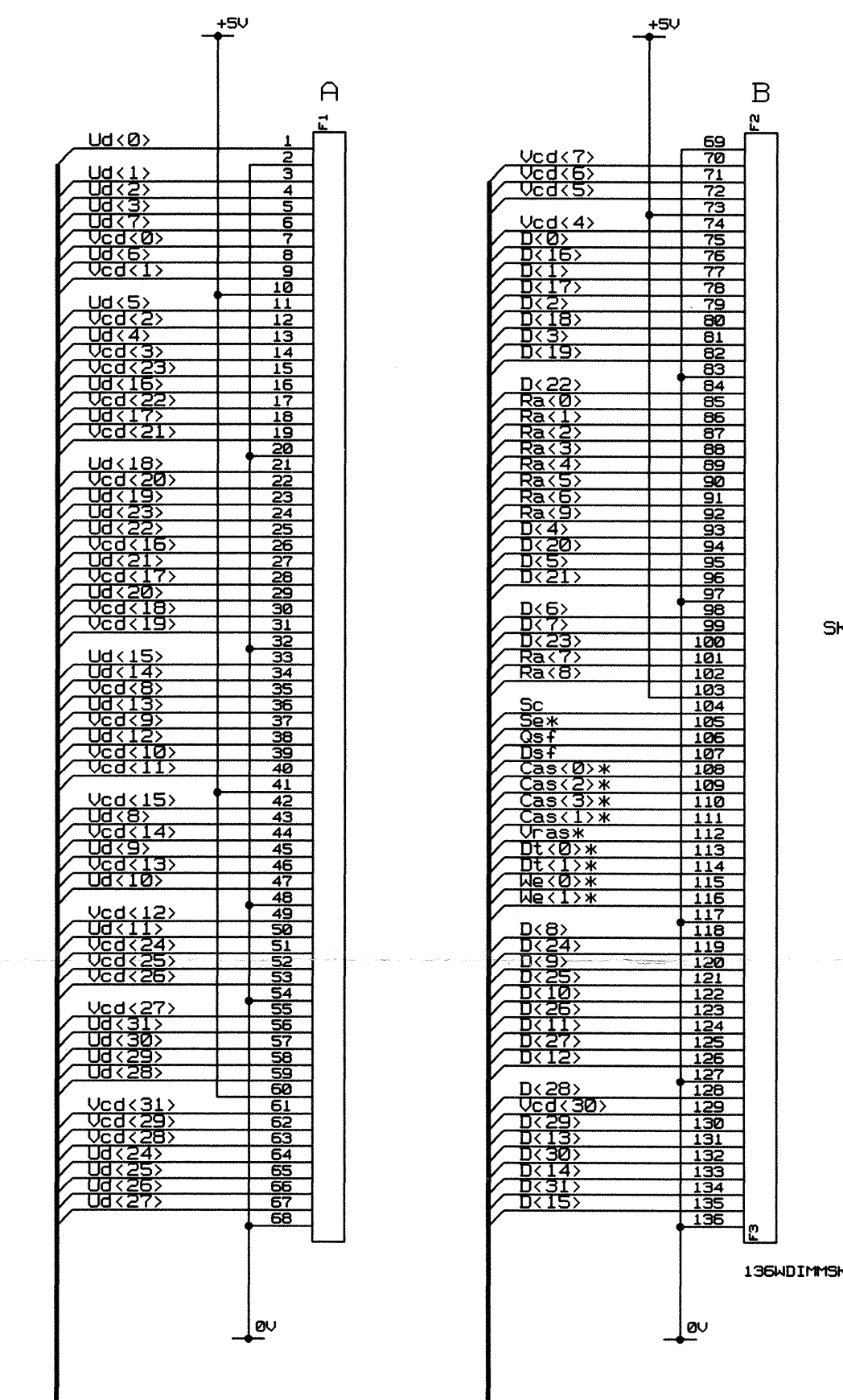
DRAM & VRAM



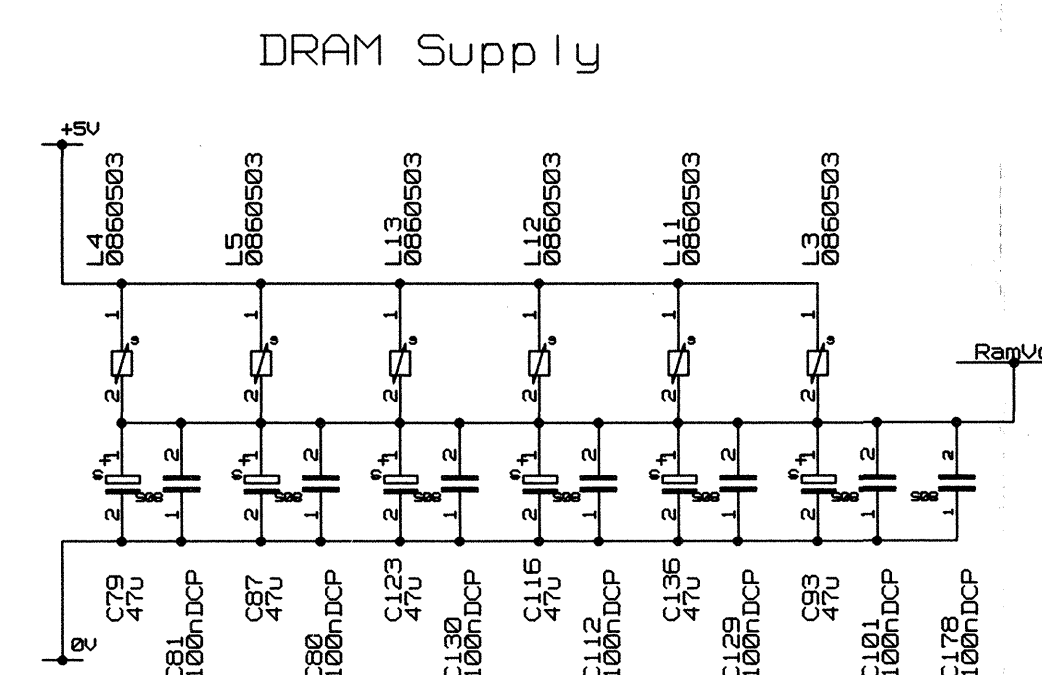
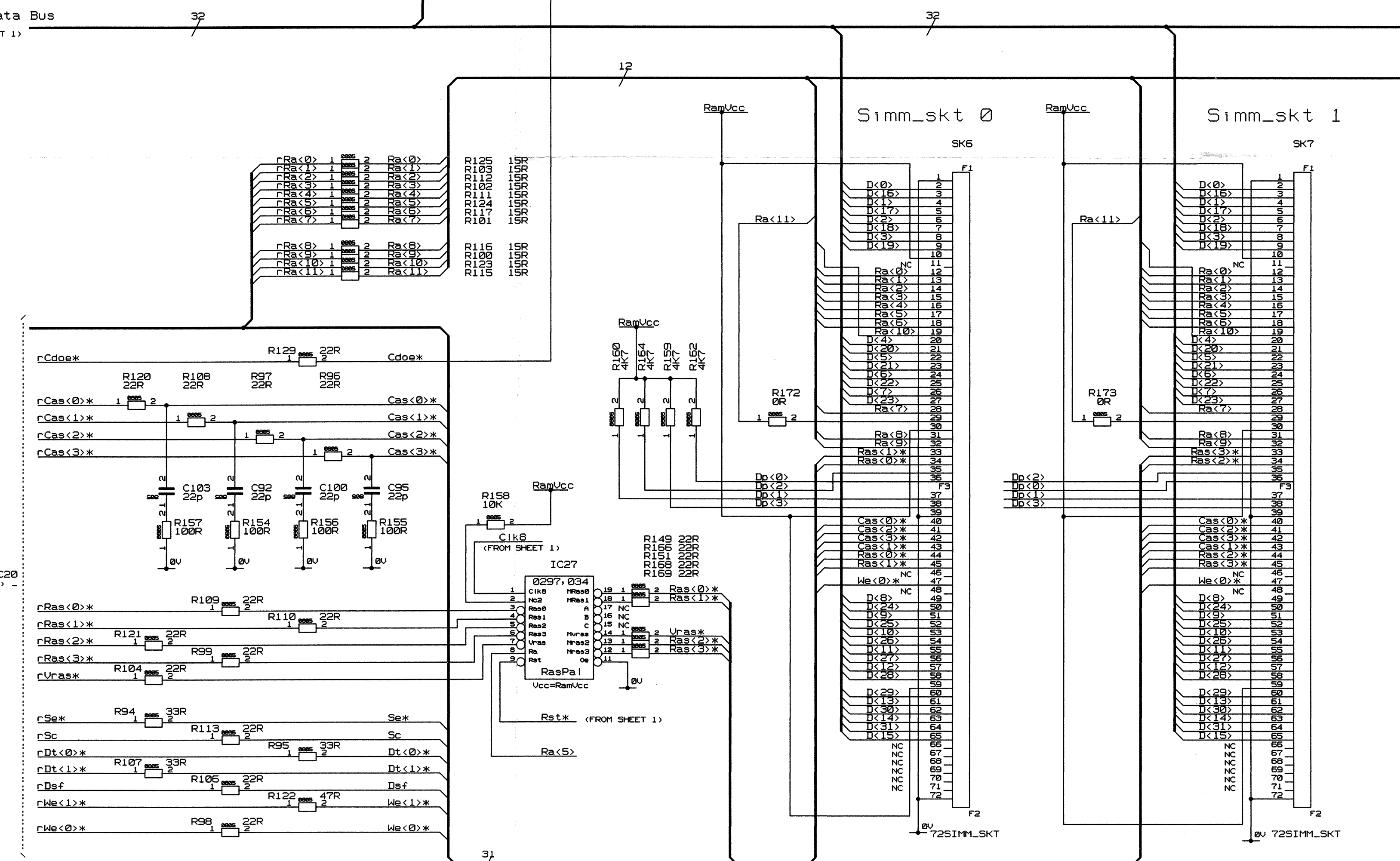
VIDC20 Lower 32 data bits (TO SHEET 5)

VIDC20 Upper 32 data bits (TO SHEET 5)

1MB/2MB VRAM Expansion Card



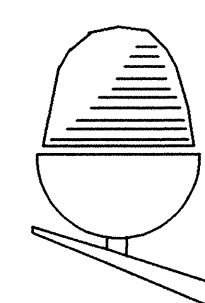
NOTE:
ISSUE 2 OF THIS SCHEMATIC RELATES
TO THE ISSUE 1 PCB ASSEMBLY PLUS
ENGINEERING DEVIATIONS LISTED.



2	11-04-94	EC03427 No changes this sheet	04
1	27-01-94	PRR 3904	05
ISS	DATE	DESC. OF CHANGE	ENG. CHK

SCALE	_____	ALL DIMENSIONS IN mm UNLESS OTHERWISE STATED	
MATERIAL	_____		
FINISH	_____		
WHOLE NO.	TOL. _____	DECIMAL No.	TOL. _____
		ANGLE TOL.	_____

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CAMBRIDGE
CB4 4AF

ACORN COMPUTERS Ltd

TITLE	MEDUSA MAIN PCB CIRCUIT DIAGRAM	Sht 3/7
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1	DRG. No	0197, 000	/C
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