PCMCIA CONTROLLER ASIC

- ARM Based PCMCIA Controller Solution.

- Implemented in a 0.8 micron, Low Power CMOS process.

- Supports,

  - Dual PCMCIA Slots.

  - Both Memory and I/O Card.

  - XIP for 32 bit operations.

  - 8M bytes of contiguous memory space for each PCMCIA slot.

  - 64M bytes of PCMCIA address space.

  - Two General Purpose I/O ports, Ideal for additional system control.

- Programmable,

  - CardVoltage Control, for FLASH cards.

  - Buffer Control, to allow Hot insertion of cards.

  - Address mapping.

  - Card Access timing.

- RISC-OS PCMCIA software modules available.

- Adheres to PCMCIA Standards Release 2.1 / Jedia 4.2. (July 93).

- Full specification Data sheet available.

- Also available as:

  - Single Euro Expansion Card, Ideal for development systems.

  - Supports PCMCIA card type I, II, and III.

  - Macrocell, Custom variants available, ideal for Custom ASIC solutions.

Outline Specification

Electrical Characteristics

- Temperature Range   –20 to +75°C

- Voltage Range   5 Volt + 10%

- Current Consumption   Stand-by 100μA

       Typical 1I0 - 160mA

- Typical Clock Frequency 16MHz

Mechanical Characteristics

- Dimensions $31.2 \pm 0.3\text{mm}^2$ x 3.56mm

- Package type   136 pin QFP

Programmable

- Interrupt support via 4 registers. Request, Mask, Status and Clear

- PCMCIA Slot Configuration and control via 5 registers per slot

- Paging, Status, Access mode, Access time and Voltage control

- 9 General purpose I/O ports

Card Accessing

- 8, 16 and 32-bit access support using XIP access cycles